Remarks

The above-referenced application has been reviewed in light of the Examiner's Final Office Action dated August 22, 2007. The Examiner's allowance of Claim 21 is gratefully acknowledged. Claims 1, 5, 9 and 16 have been amended. Therefore, Claims 1-21 are currently pending in this application. The Examiner's reconsideration of the rejections is respectfully requested, particularly in view of the above amendments and the following remarks.

In accordance with the Office Action, Claims 1, 3-5, 7 and 8 stand rejected under 35 U.S.C. § 102(e) as being anticipated by U.S. Patent No. 6,574,688 to Dale et al. Claims 1 and 5 have been amended. Support for these amendments is intrinsic to the application as originally filed. *See also,* Application at page 7, line 20 through page 9, line 16. No new matter has been added.

Amended Claim 1 recites, *inter alia,* "A monolithic integrated circuit device comprising: a first port for inputting write data directly from the outside of the device and outputting read data directly to the outside of the device; and a second port for only inputting write data directly from the outside of the device . . .." Amended Claim 5 recites, *inter alia,* "An integrated circuit system comprising: a monolithic integrated circuit device that includes a first port for inputting write data directly from the outside of the system and outputting read data directly to the outside of the system and a second port for only inputting write data directly from the outside of the system . . .."

The '688 to Dale et al. is generally directed towards an apparatus for connecting function modules within a communications device or computer. *See, e.g.,* Dale at

Abstract. The Examiner relies on Dale, Figures 2 and 3, for a showing of the first and second ports. *See* O.A. at 3.

Referring to Figure 2 of Dale, it is respectfully submitted that the ports A-G are not connected to the outside of Dale's communication device or computer system, such as to external devices 151-159. To the contrary, ports A-G are clearly shown as connected within the inside of Dale's communication device or computer system. That is, ports A-G connect to function modules 101-109, which are internal modules.

Accordingly, Dale fails to teach or suggest "monolithic integrated circuit device comprising: a first port for inputting write data directly from the outside of the device and outputting read data directly to the outside of the device; and a second port for only inputting write data directly from the outside of the device" as recited in Applicant's amended Claim 1. In addition, Dale fails to teach or suggest "system comprising: a monolithic integrated circuit device that includes a first port for inputting write data directly from the outside of the system and outputting read data directly to the outside of the system and a second port for only inputting write data directly from the outside of the system" as recited in Applicant's amended Claim 5. Therefore, amended Claims 1 and 5 are neither taught nor suggested by the '688 to Dale et al.

In accordance with the Office Action, Claims 2, 6, 9-15 and 16-20 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over U.S. Patent No. 6,574,688 to Dale et al. Claims 1, 5, 9 and 16 have been amended. Support for these amendments is intrinsic to the application as originally filed. *See also,* Application at page 7, line 20 through page 9, line 16. No new matter has been added.

Amended Claim 9 recites, *inter alia,* "A monolithic integrated circuit device comprising: a first port for inputting write data directly from the outside of the device and outputting read data directly to the outside of the device; a first buffering unit . . . a second port for only inputting write data directly from the outside of the device; a second buffering unit . . . and a selecting unit . . .." Amended Claim 16 recites, *inter alia,* "A monolithic integrated circuit device comprising: a first port for inputting write data directly from the outside of the device and outputting read data directly to the outside of the device; an input/output buffer . . . a second port for only inputting write data directly from the outside of the device; an input buffer . . . and a register . . .."

The '688 to Dale et al. was discussed above with respect to the § 102(e) rejections, and said discussion is similarly applicable here. Thus, Dale et al. fail to teach or suggest "monolithic . . . device comprising: a first port for inputting write data directly from the outside of the device and outputting read data directly to the outside of the device; a first buffering unit . . . a second port for only inputting write data directly from the outside of the device" as recited in amended Claim 1. Similarly, Dale et al. fail to teach or suggest "monolithic . . . device comprising: a first port for inputting write data directly from the outside of the device and outputting read data directly to the outside of the device; an input/output buffer . . . a second port for only inputting write data directly from the outside of the device" as recited in amended Claim 16. Therefore, amended Claims 9 and 16 are neither taught nor suggested by the '688 to Dale et al., whether taken alone or in combination with any of the other references of record in this case.

Conclusion

Accordingly, it is respectfully submitted that amended independent Claims 1, 5, 9 and 16 are in condition for allowance for at least the reasons stated above, Claim 21 having been allowed. Since the remaining claims each depend from one of the above claims and necessarily include each of the elements and limitations thereof, it is respectfully submitted that these claims are also in condition for allowance for at least the reasons stated, as well as for reciting additional patentable subject matter. Thus, each of Claims 1-21 is in condition for allowance. All issues raised by the Examiner having been addressed, reconsideration of the rejections and an early and favorable allowance of this case are earnestly solicited.

Respectfully submitted,

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